

Figure 1A

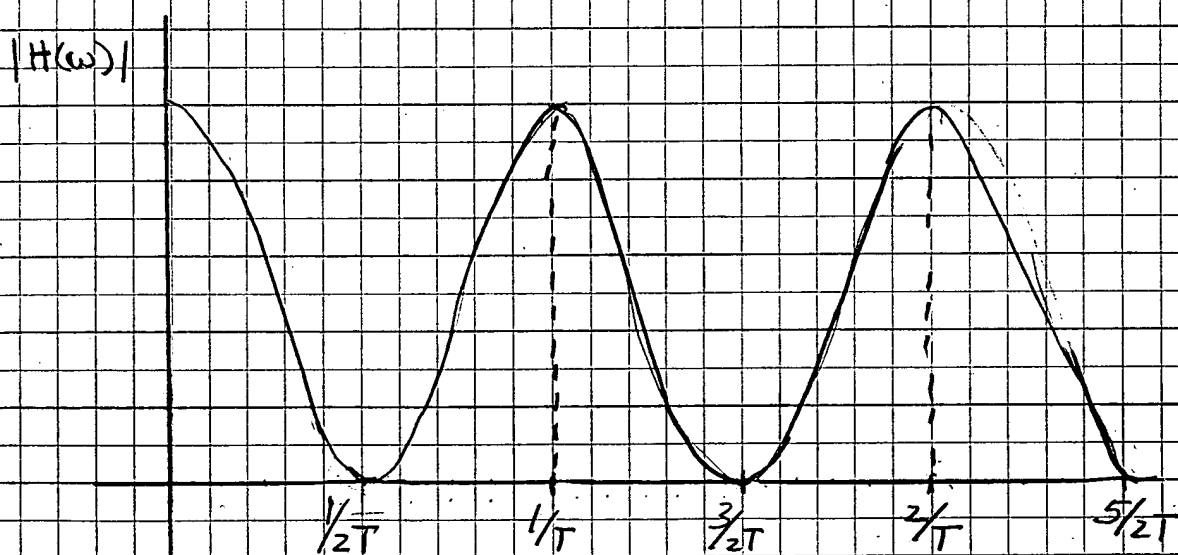


Figure 1B

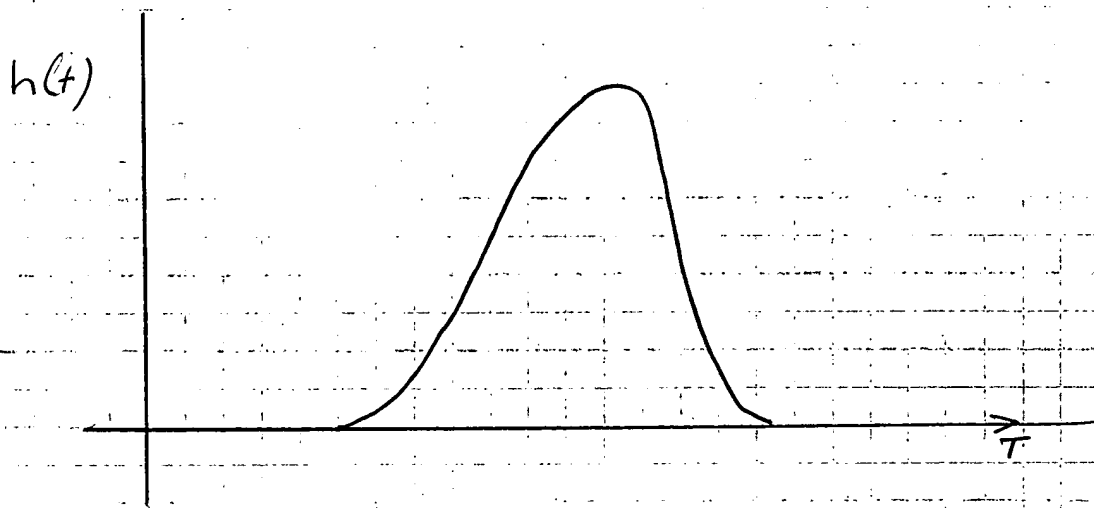


Figure 1C.

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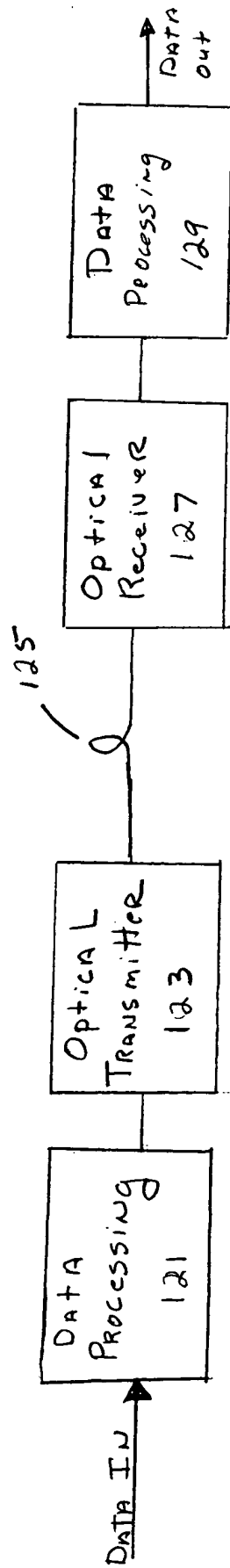


Figure #1D

Multicarrier Modulation Block Diagram (Transmitter)

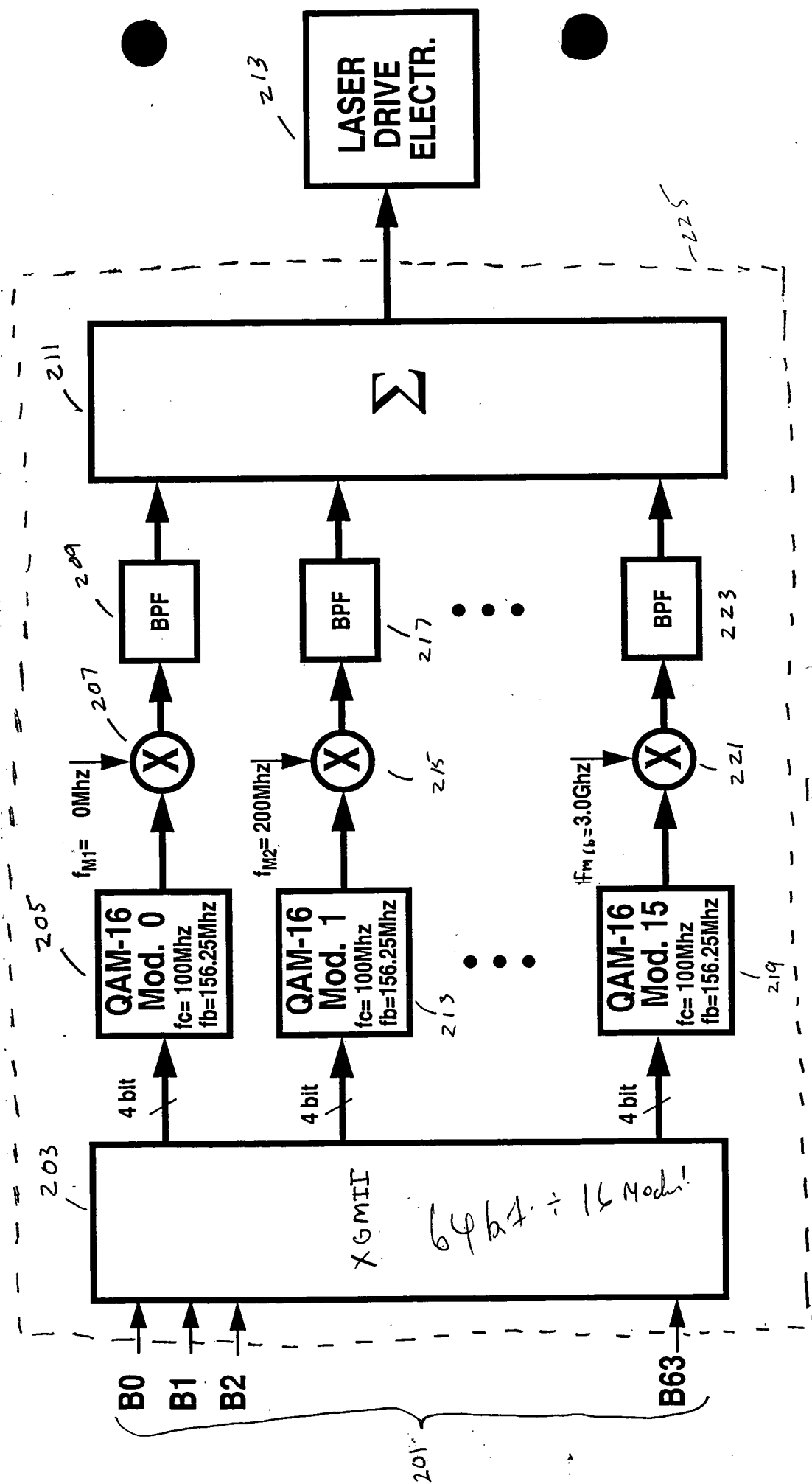
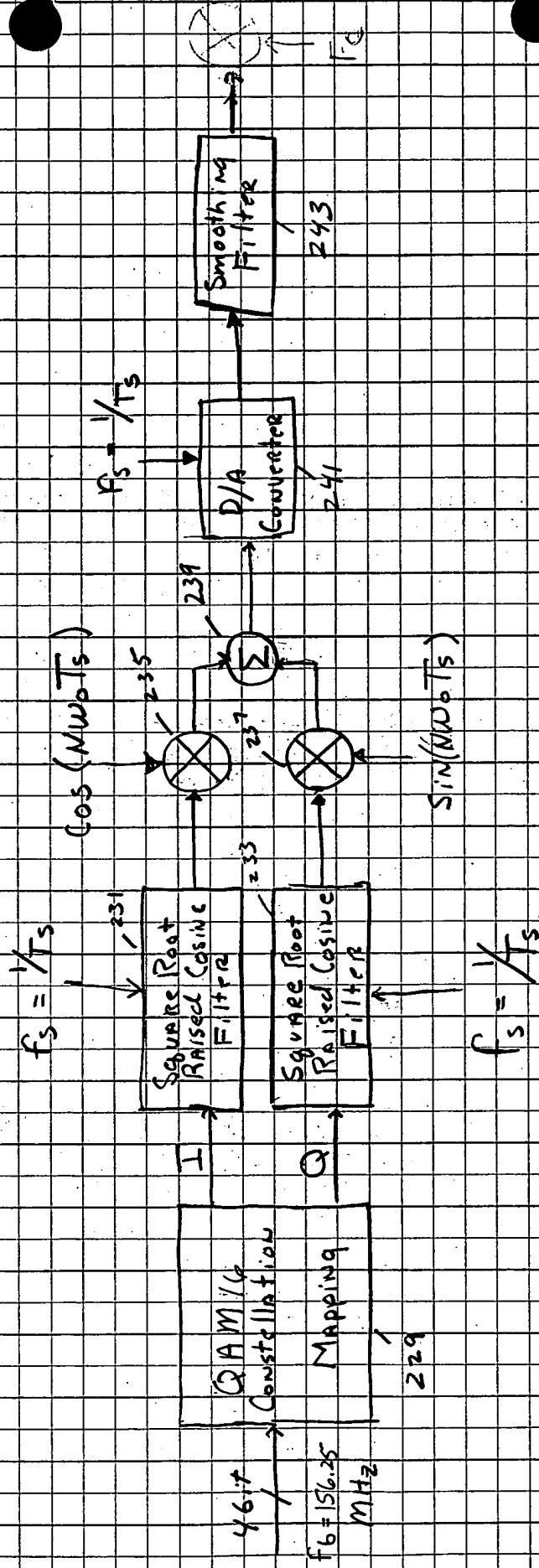


Figure 2

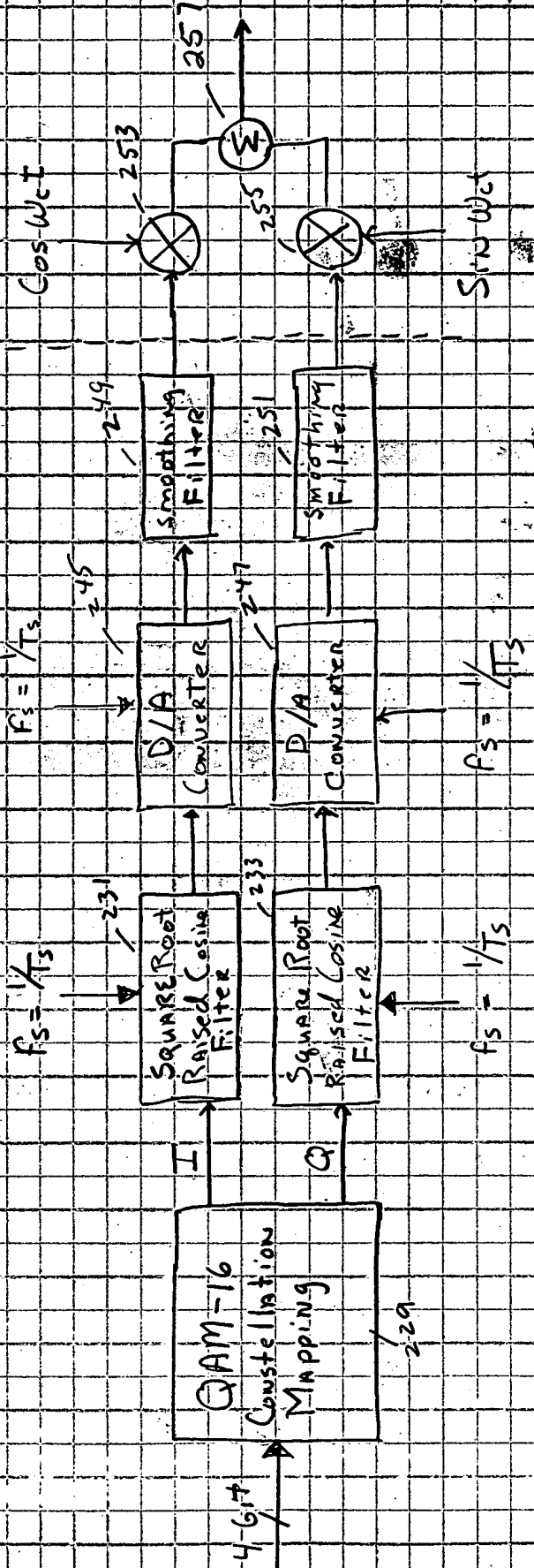
205



T_s = Sampling period - digital
 $\omega_N = 2\pi f_s$ = digital carrier angular frequencies.

Figure 2.13

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$T_s = \text{Sampling period for DSP blocks}$
 $\omega_c = 2\pi f_c = \text{ANALOG CARRIER ANGULAR FREQUENCY}$

Figure 2C

Multicarrier Modulation Block Diagram (Receiver)

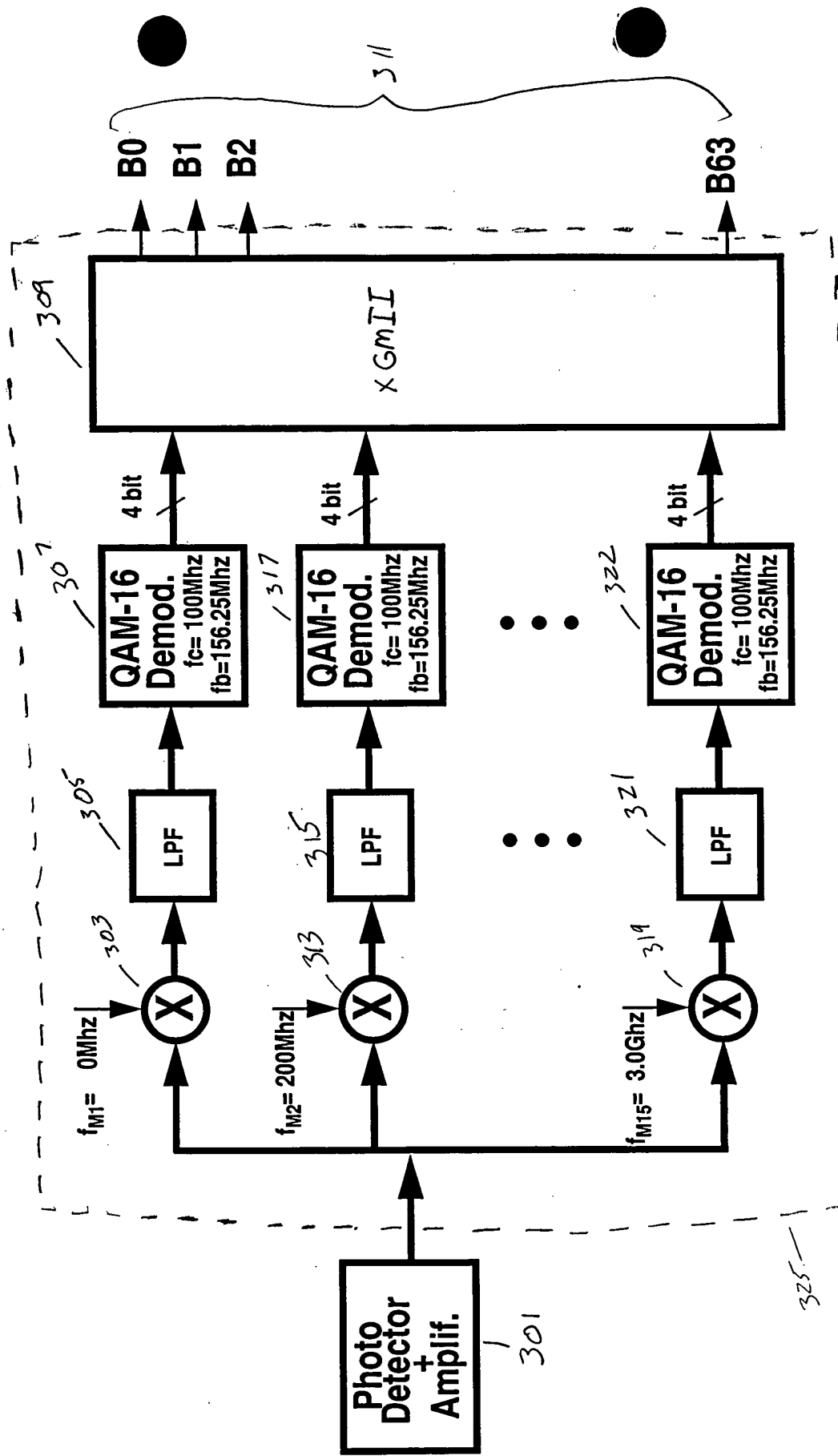


Figure 3

Alternative Implementation of Multicarrier Modulation (Transmitter)

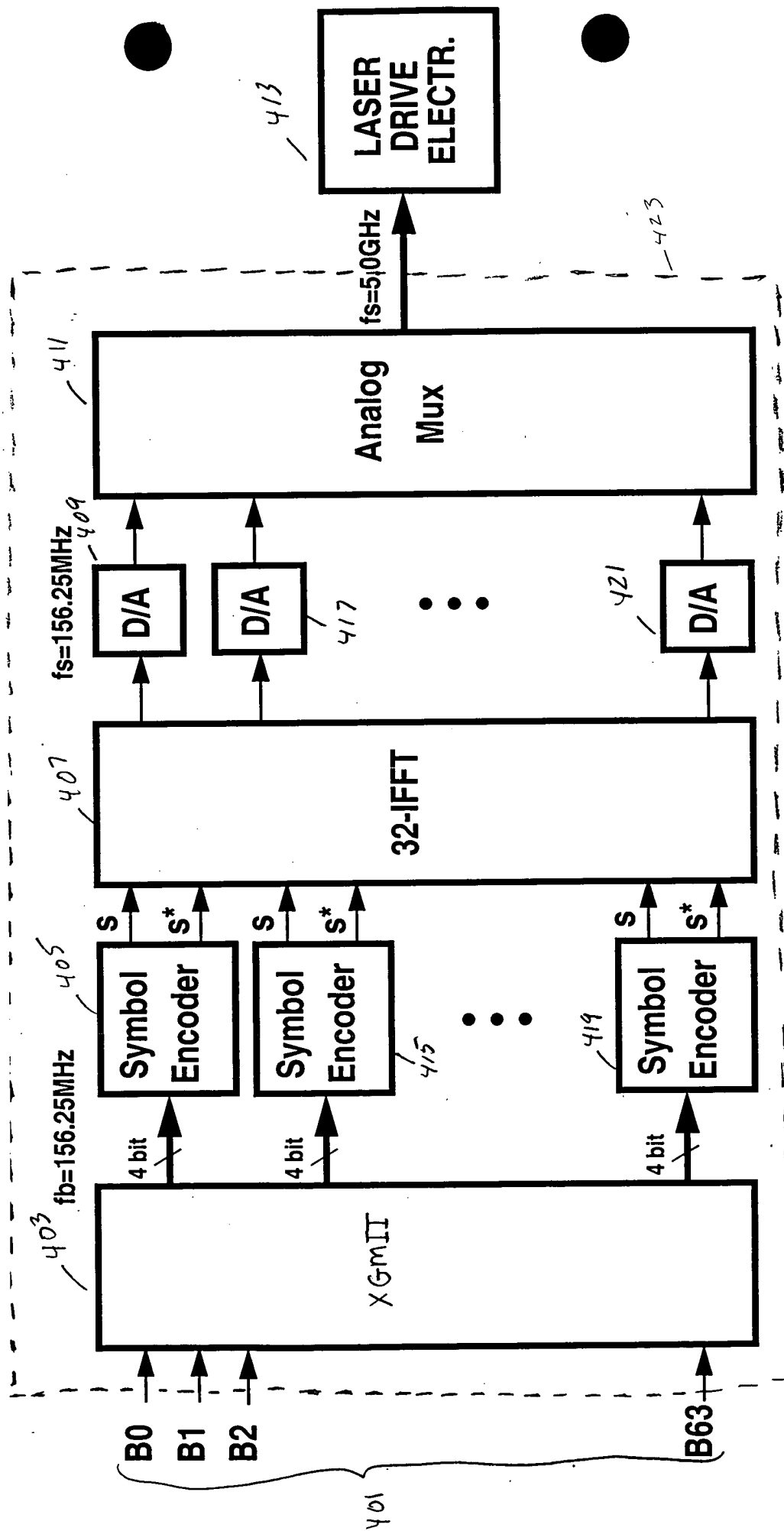


Figure 4

Alternative Implementation of Multicarrier Modulation (Receiver)

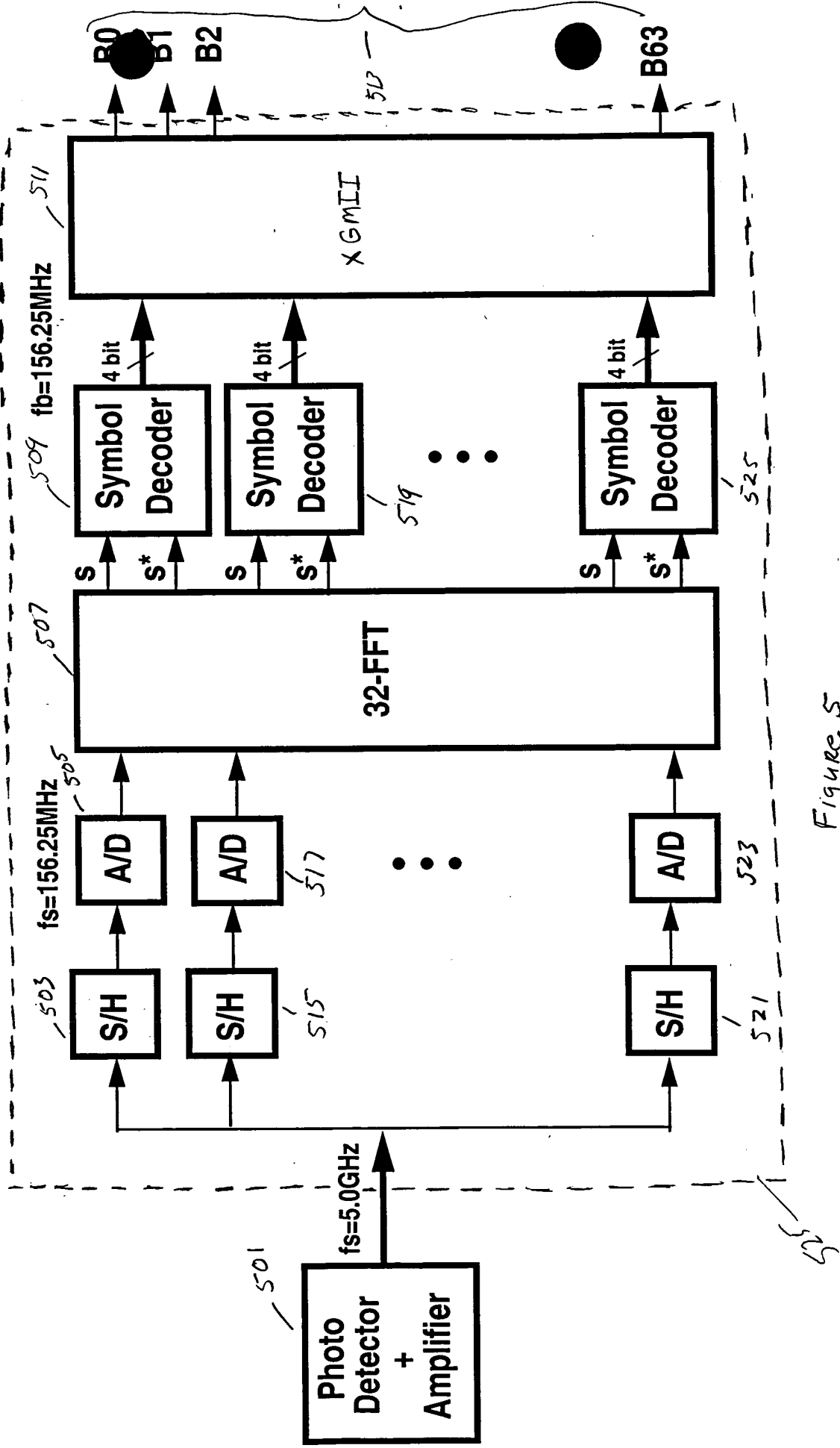
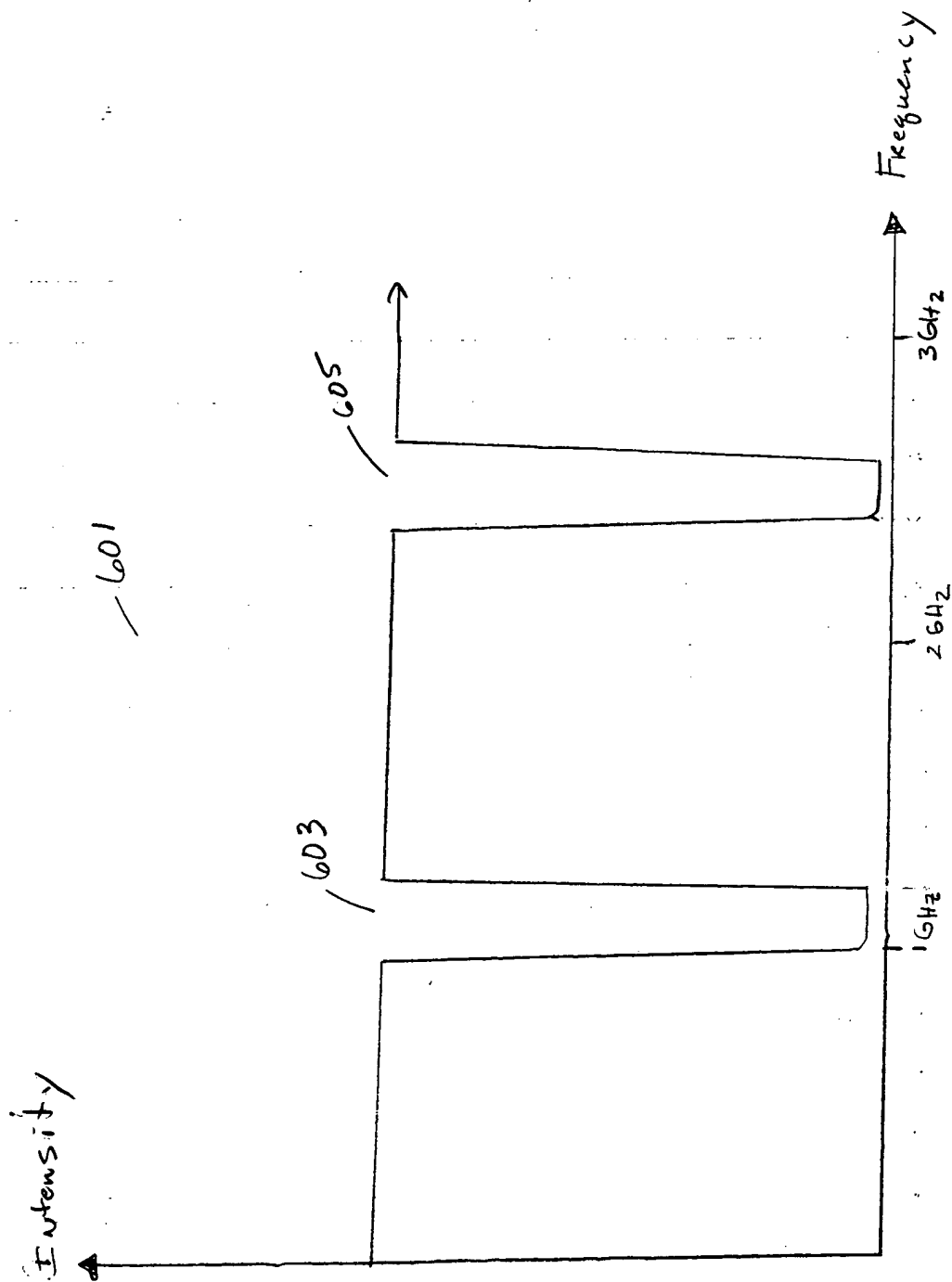


Figure 5

$\frac{d}{dt} \left(\frac{\partial L}{\partial \dot{x}} \right) = \frac{\partial L}{\partial x}$



109

603

505

Frequency

3642

2642

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Figure 6

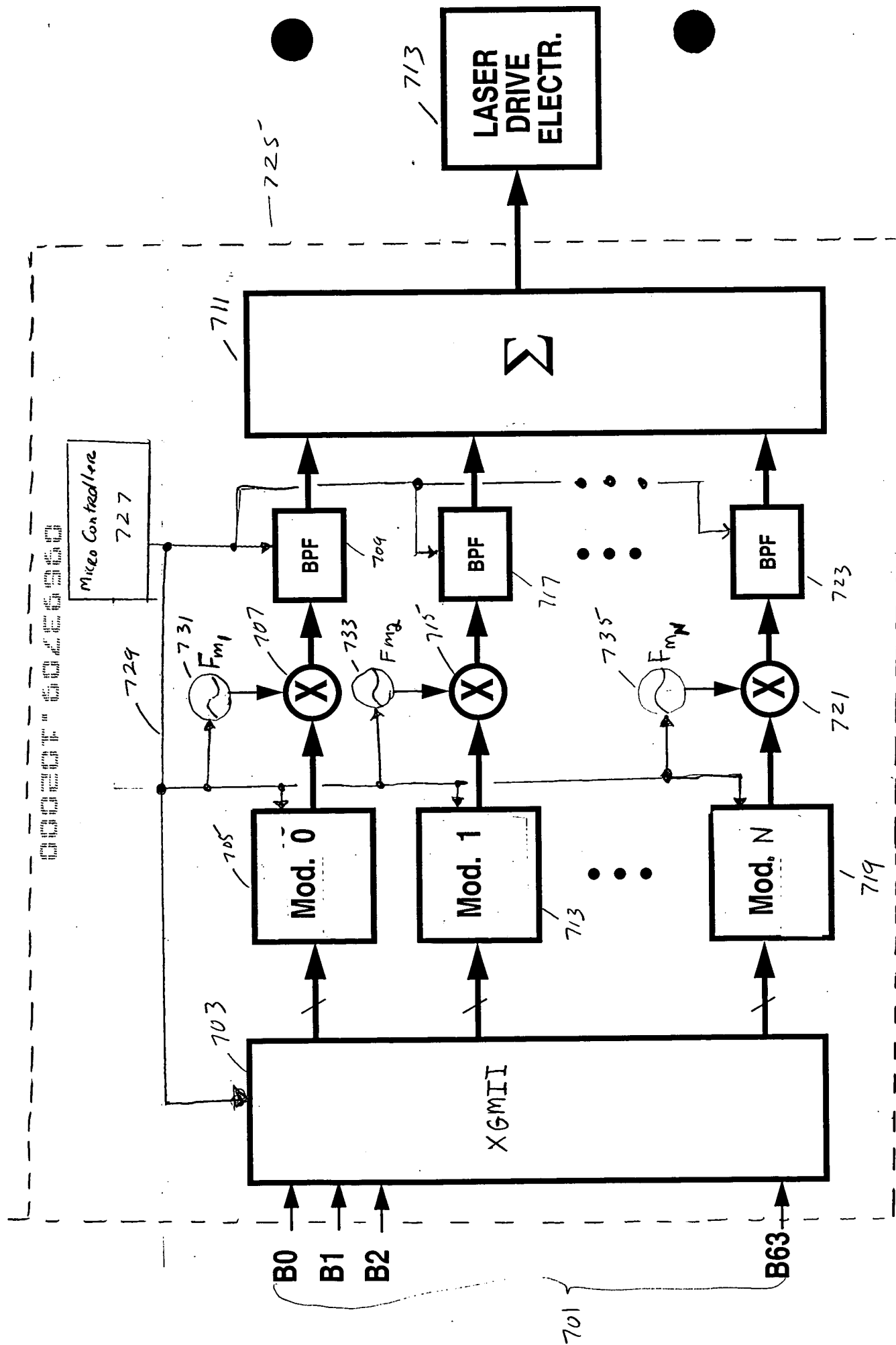


Figure 7A

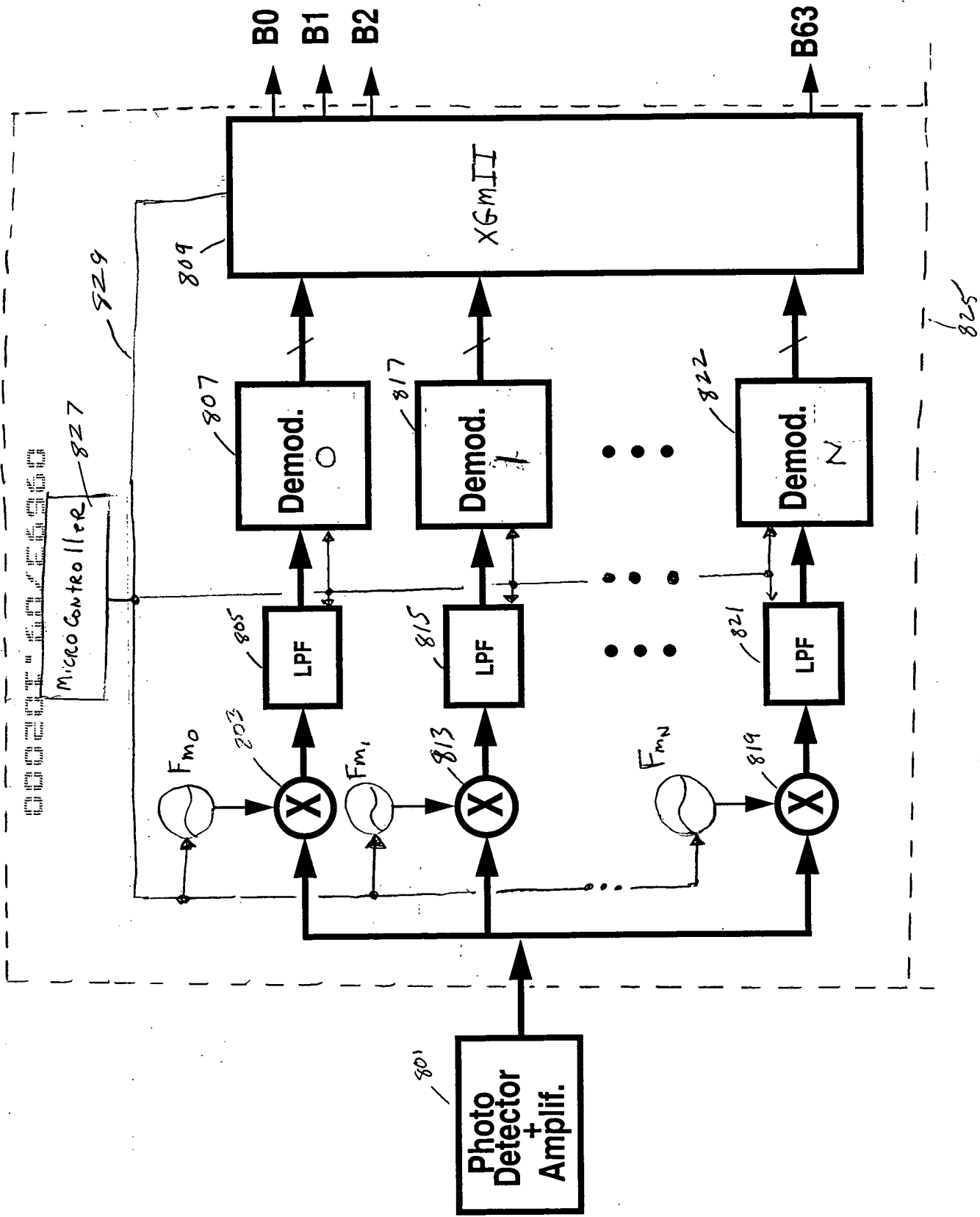


Figure 8

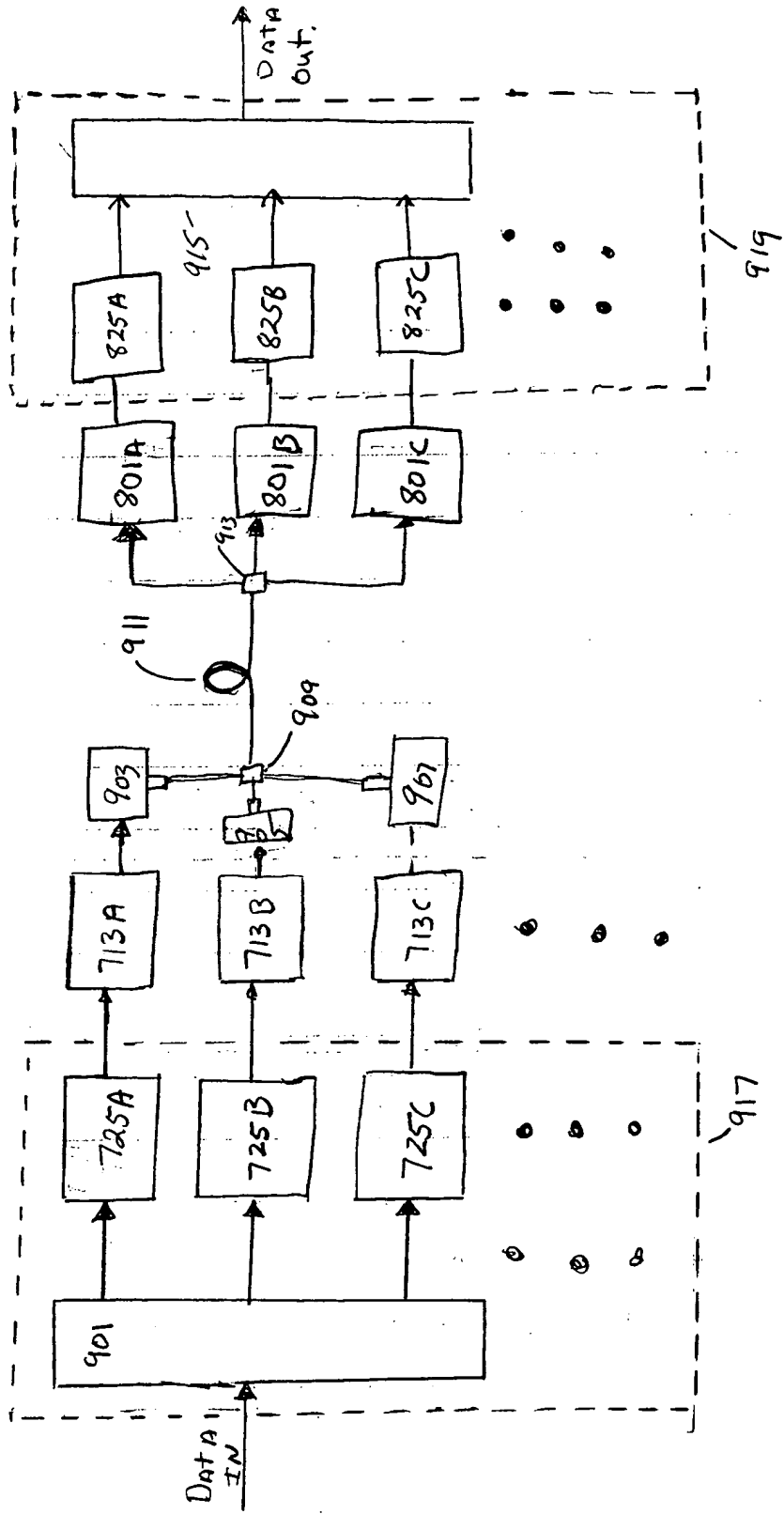


Figure 9

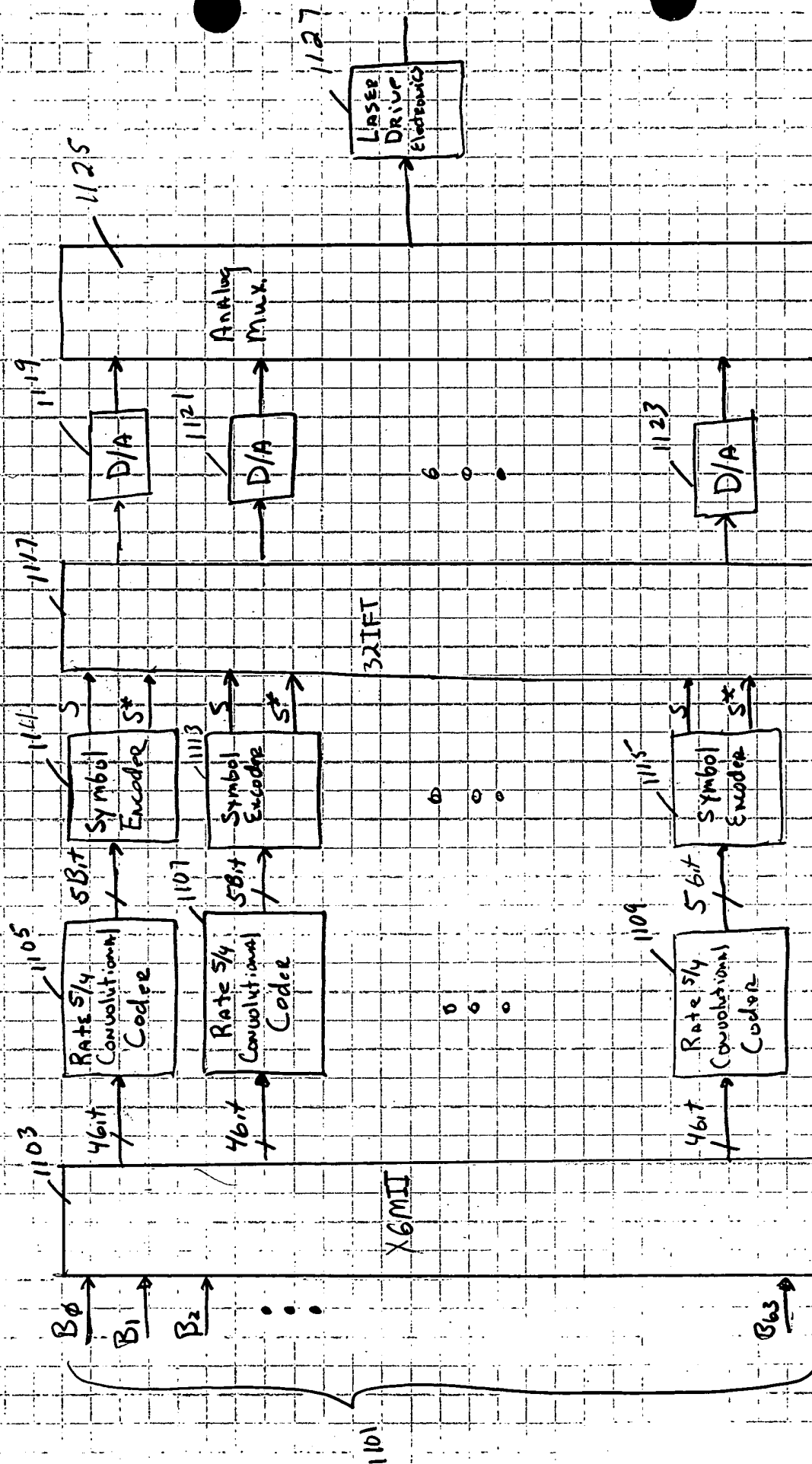


Figure 11

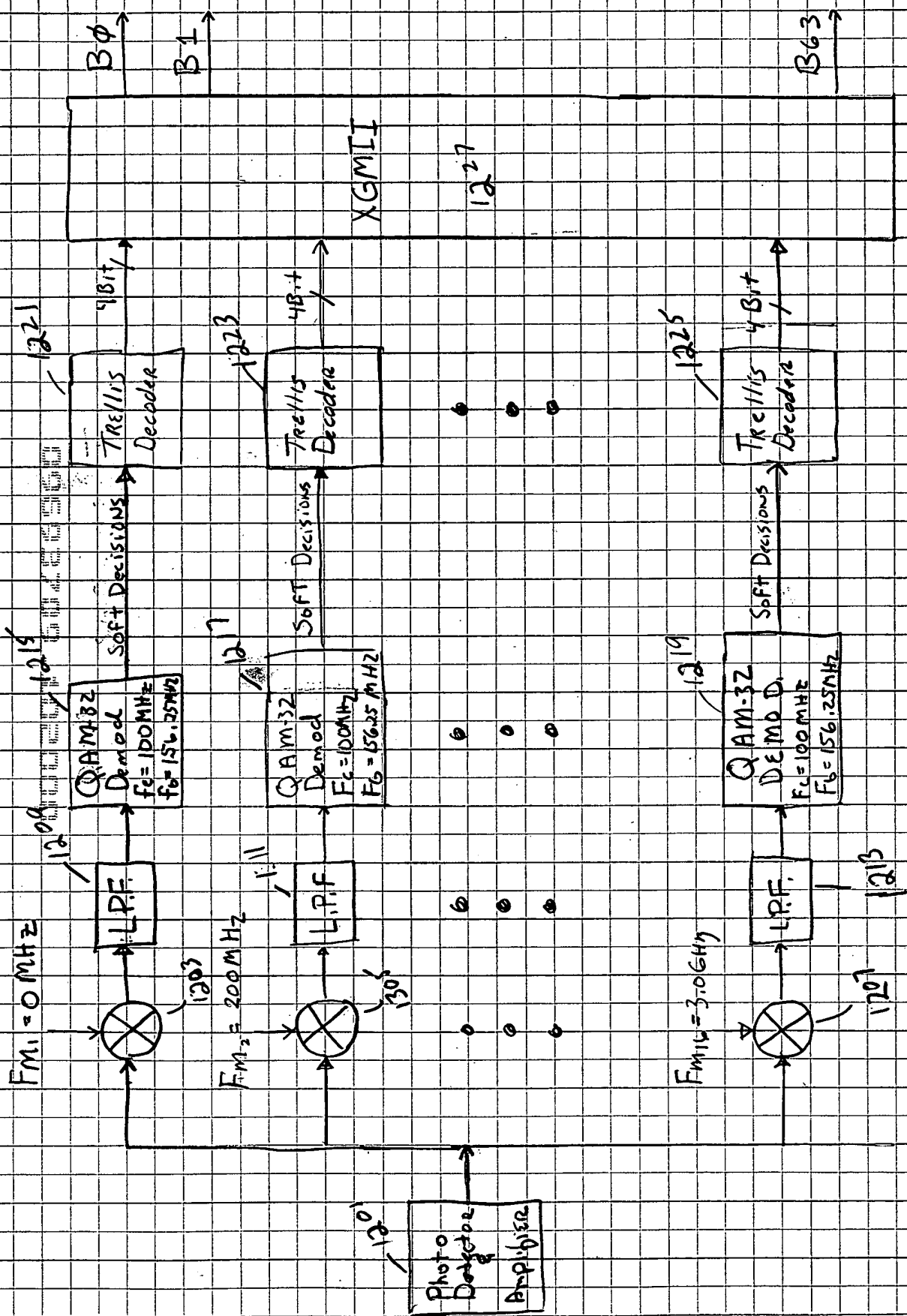


Figure 125

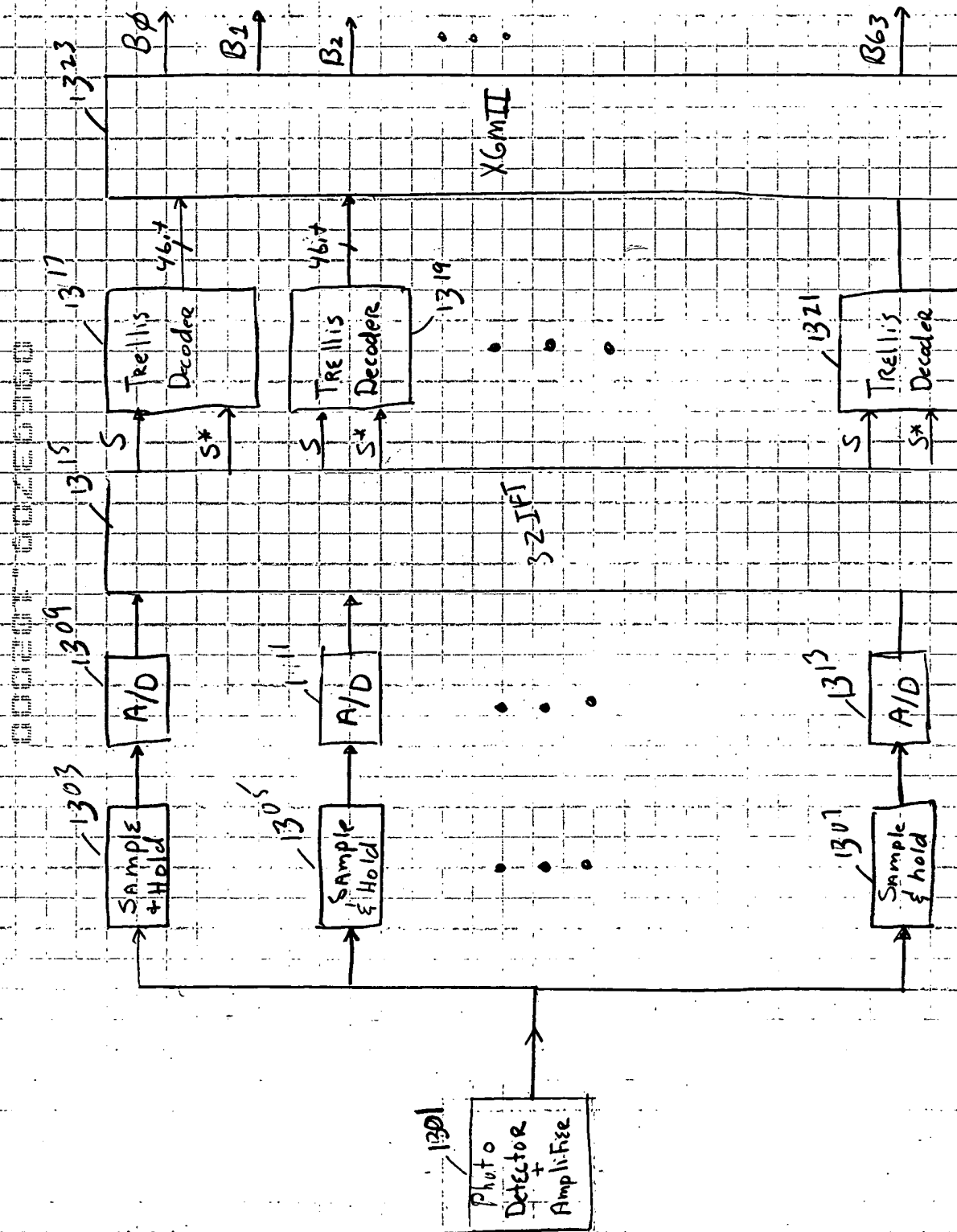


Figure 13